

ABSTRACT

Disclosed are a data writing apparatus and a data writing/reading apparatus which can allow an processor to properly complete data writing and reading when a double failure occurs, reads data from an address at which a failure has occurred during data correction at that address and completes writing data in a hard disk device when correction of data at the address at which the failure has occurred is completed. When data writing at a logical address of a logical disk is disabled due to a double failure, logical disk writing/reading means writes the data in a memory and reports proper completion of writing to an processor. The processor reads the data from the memory. When reported from write-enableness reporting means that writing at the logical address becomes enabled, the logical disk writing/reading means writes the data in the memory at the logical address of the logical disk.